

### Claim Amendments

Claim 1 (currently amended): A switch for switching packets, each packet having a length, comprising:

port cards which ~~receives~~ receive packets of different lengths from and sends packets to a network; and

fabrics connected to the port cards which switch the packets, the port cards sending stripes of corresponding fragments of each packet to each of the fabrics, each fabric having a memory mechanism having a memory which has a wide cache buffer structure in which multiple packets are put into one word, each fabric having a mechanism for determining the length of each packet received by the fabric and placing a length indicator with the packet so when the packet is stored in the memory of the memory mechanism, the determining mechanism can identify from the length indicator how long the packet is and where the packet ends in the memory of the memory mechanism.

Claim 2 (previously presented): A switch as described in Claim 1 wherein the determining mechanism includes an aggregator which receives the stripes of the packet

fragments from the port cards, determines the packet length and appends packet length information to the beginning of the packet in the length indicator.

Claim 3 (original): A switch as described in Claim 2 wherein the memory mechanism includes a memory controller, the aggregator sending the packet with the packet length information to the memory controller which stores the packet with the packet length information.

Claim 4 (currently amended): A switch as described in Claim 3 wherein the memory controller has ~~[[a]]~~ the memory which has a wide cache buffer structure in which multiple packets are put into one word.

Claim 5 (previously presented): A switch as described in Claim 4 wherein the fabric includes a separator which reads the stripes of fragments of packets from the memory controller and extracts the packet length information from each stripe of fragments of packet to determine when each packet ends, and sends fragments of the packet as stripes to the port cards, the port cards assembling the packets from the stripes of the fragments of the packets the port cards receive from the fabrics.

Claim 6 (previously presented): A switch as described in Claim 5 wherein the separator removes the packet length information from each packet before sending any stripes of fragments of each packet to an unstriper of the port cards.

Claim 7 (currently amended): A method for switching packets having a length comprising the steps of:

receiving packets of different lengths at [[a]] port cards of a switch;

sending corresponding fragments of each of the packets as stripes to each fabric of a plurality of fabrics of the switch from the port cards;

receiving the stripes of corresponding fragments of each of the packets at each of the fabrics of the switch;

measuring the length of the packets at each fabric from the stripes of fragments of the packets received at each fabric;

appending a length indicator to each packet;

storing each packet received at each fabric with the length indicator in a memory of a memory mechanism of the fabric, the memory which has a wide cache buffer structure in which multiple packets are put into one word;

reading each packet from the memory mechanism; and

determining where each packet ends from the length indicator of each packet.

Claim 8 (previously presented): A method as described in Claim 7 wherein the step of receiving the fragments includes the step of receiving the fragments at an aggregator of each of the fabric.

Claim 9 (previously presented): A method as described in Claim 8 wherein the measuring step includes the step of measuring the length of each packet with the aggregator.

Claim 10 (previously presented): A method as described in Claim 9 wherein the appending step includes the step of the appending the length indicator to each packet with the aggregator.

Claim 11 (previously presented): A method as described in Claim 10 wherein the storing step includes the step of storing each packet with the length indicator in a memory controller of the memory mechanism.

Claim 12 (previously presented): A method as described in Claim 11 wherein the reading step includes the step of reading each packet from the memory controller with a separator of the fabric.

Claim 13 (original): A method as described in Claim 12 wherein the determining step includes the step of determining where a packet ends from the length indicator with the separator.

Claim 14 (previously presented): A method as described and Claim 13 including after the determining step, there is the step of removing the packet length information with the separator.

Claim 15 (previously presented): A method as described in Claim 14 including after the removing step, there is the step of sending stripes of fragments of the packets from the separator to the port card.

Claim 16 (previously presented): A method as described in Claim 15 wherein the sending fragments step includes the step of sending fragments of the packet to the port cards in a same logical time with corresponding fragments of the packet from other fabrics to the port cards.

Claim 17 (currently amended): A method as described in Claim 16 wherein the storing step includes the step of storing the fragments of the packet in ~~[[a]]~~ the memory of the memory controller which has a wide cache buffer structure in which multiple packets are put into one word.

Claim 18 (previously presented): A method as described in Claim 17 including after the reading step, there is the step of extracting the packet length information from the packet with the separator.

Claim 19 (previously presented): A method as described in Claim 18 wherein there is a step of receiving the stripes of fragments of the packet from the fabrics with an unstriper of the port cards.

Claim 20 (previously presented): A method as described in Claim 19 wherein the sending stripes of fragments of the packet to the fabric step includes the step of sending the

stripes of fragments of the packet with a striper of the port cards to the aggregator of each fabric.

Claim 21 (previously presented): A method as described in Claim 20 wherein the step of sending fragments to the port cards includes the step of sending fragments from the separator to an unstriper of the port cards.